

COF c

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Thomas P. Glenn, Roy Dale Hollaway, Steven Webster  
Assignee: Amkor Technology, Inc.  
Title: WAFER HAVING ALIGNMENT MARKS EXTENDING FROM A  
FIRST TO A SECOND SURFACE OF THE WAFER  
Serial No.: 09/803,084 Filing Date: March 8, 2001  
Patent No.: 6,943,429 B1 Issued: September 13, 2005  
Examiner: Williams, Group Art Unit: 2826  
Alexander O.  
Docket No.: G0049

Monterey, CA  
December 19, 2005

ATTENTION: CERTIFICATE OF CORRECTIONS BRANCH  
COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA. 22313-1450

Certificate  
DEC 28 2005  
of Correction

REQUEST FOR ENTRY OF  
CERTIFICATE OF CORRECTION

Sir:

Please enter the enclosed Certificate of Correction (PTO Form 1050) in the above patent.

The errors sought to be corrected were made by

- ☒ The Patent and Trademark Office as explained below. Thus, no fee is required for the Certificate of Correction pursuant to 37 CFR §1.322.
- ☐ Applicant(s) (at least in part). See next section for explanation. This appropriate fee under 37 CFR §1.323 has been authorized below.

Attached as Exhibit A (1 page) is a copy of the relevant page of the Amendment as filed on January 13, 2005, which supports the requested corrections to Claims 22, 23 and 24, and

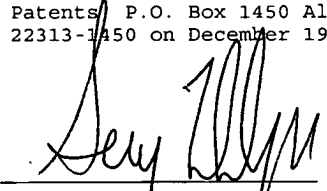


shows that the errors were made by the U.S. Patent and Trademark Office.

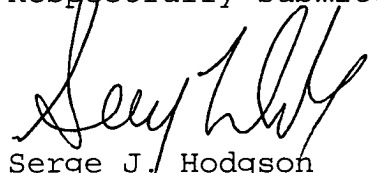
As shown in Exhibit A, originally numbered Claims 33, 34 and 35 (renumbered in the Patent as Claims 22, 23 and 24) are dependent on originally numbered Claim 32 (renumbered in the patent as Claim 21). The patent shows Claims 22, 23 and 24 as dependent on Claim 16 in error. Claims 22, 23 and 24 are dependent on Claim 21, not on Claim 16.

The Commissioner is hereby authorized to charge any fees required for consideration and entry of the enclosed documents, and to credit any overpayment of fees to Deposit Account No. 50-0553.

Please direct all inquiries concerning this request to the undersigned attorney.

|  |                   |
|--|-------------------|
| I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450 Alexandria, VA. 22313-1450 on December 19, 2005. |                   |
|   | December 19, 2005 |
| Attorney for Applicant(s)  | Date of Signature |

Respectfully submitted,

  
Serge J. Hodgson  
Attorney for Applicant(s)  
Reg. No. 40,017  
(831) 655-0880

31. (previously presented) The wafer of Claim 23 wherein said scribe grid comprises an etched silicon oxide layer.

32. (previously presented) The wafer of Claim 23 further comprising electronic components delineated by said scribe grid.

33. (previously presented) The wafer of Claim 32 wherein said electronic components are selected from the group consisting of integrated circuits, micromachine chips and image sensor chips.

34. (previously presented) The wafer of Claim 32 wherein said electronic components comprise bond pads coupled to said first surface.

35. (previously presented) The wafer of Claim 32 wherein said electronic components comprise active areas coupled to said first surface.

36. (previously presented) The wafer of Claim 25 wherein said vertical scribe line extends in a first direction and wherein said horizontal scribe line extends in a second direction, said wafer further comprising a flat extending in said second direction.

37. (previously presented) A wafer comprising:  
a front-side surface;  
a back-side surface;  
a first scribe line coupled to said front-side surface;  
and

a first back-side alignment mark extending from said front-side surface to said back-side surface, said first back-side alignment mark being formed along said first scribe line.

## UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 6,943,429 *Bi* Page 1 of 1  
APPLICATION NO. : 09/803,084  
ISSUE DATE : September 13, 2005  
INVENTOR(S) : Thomas P. Glenn, Roy Dale Hollaway, Steven Webster

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Claim 22, at Column 10, Line 48, change "claim 16" to --claim 21--.  
In Claim 23, at Column 10, Line 51, change "claim 16" to --claim 21--.  
In Claim 24, at Column 10, Line 53, change "claim 16" to --claim 21--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

GUNNISON, McKAY & HODGSON, L.L.P.  
1900 Garden Road, Suite 220  
Monterey, CA 93940

This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

*If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2*